



Form PTO-1449

INFORMATION DISCLOSURE CITATION  
IN AN APPLICATION

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Document Number (Optional)

DS-03-007

Application Number

10/618,535

Applicant

Andreas Sibrai et al.

Filing Date

07/11/03

Group Art Unit

## U. S. PATENT DOCUMENTS

EXAMINER INITIAL	DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE
JL	5937340	8/10/99	Philippe et al.	455	310	10/7/97
JL	5920235	7/6/99	Beards et al.	331	108 D	6/25/97

## FOREIGN PATENT DOCUMENTS

	DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	Translation	
						YES	NO

## OTHER DOCUMENTS (Including Author, Title, Date, Portmanteau Pages, Etc.)

JL -	"A Fully Integrated CMOS DCS-1800 Frequency Synthesizer," by Jan Craninckx et al., IEEE Journal of Solid-State Circuits, Vol. 33, No. 12, Dec. 1998, pp. 2054-2065.
JL -	"Wide-Tuning-Range Dual-VCOs for Cable Modem Receivers," by Dari Stadiu et al., 2002 IEEE MTT-S Digest, XP-001099558, IT-TU-53, pp. 581-584.

EXAMINER

*Joseph Chang*

DATE CONSIDERED

9/15/04

EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP § 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to the applicant.

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## U. S. PATENT DOCUMENTS

DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILED DATE IF APPROPRIATE
6486744	11/26/02	Cann	331	117D	5/16/01
6353368	3/5/02	Iravani	331	57	11/9/99
6181216	1/30/01	Waight	331	117FE	7/19/99

## FOREIGN PATENT DOCUMENTS

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## OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)

~~duplicate~~  
~~"A Full Integrated CMOS DCS-1800 Frequency  
 Synthesizer", by Jan Craninckx et al., IEEE  
 Journal of Solid-State Circuits, Vol. 33, No. 12,  
 Dec. 1998, pp. 2054-2065.~~

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